Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 07977-024003	Application No. New Divisional Application
Information Disclosure Statement by Applicant (Use several sheets if necessary)		Applicant Isamu Kobori et al.	
(37 CFR §1.98(b))		Filing Date July 22, 2003	Group Art Unit Unknown

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
SDI	AA	5,413,958	05/1995	Imahashi et al.			
	AB	5,529,630	06/1996	Imahashi et al.			
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SDI	AN	05,009,089 A	01/1993	Japan				
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Other Documents (include Author, Title, Date, and Place of Publication)

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SDF	AQ	Y. Fukushima et al.; "A Poly-Si TFT Process for High Speed and Low Voltage CMOS Circuits"; *Extended Abstracts of the 1993 International Conference on Solid State Devices and Materials, Makuhari*; pp. 993-995; 1993
	AR	
	AS	
	AT	

Examiner Signature	Date Considered
Hanetta Isaac	7/10/05

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Substitute Disclosure Form (PTO-1449)